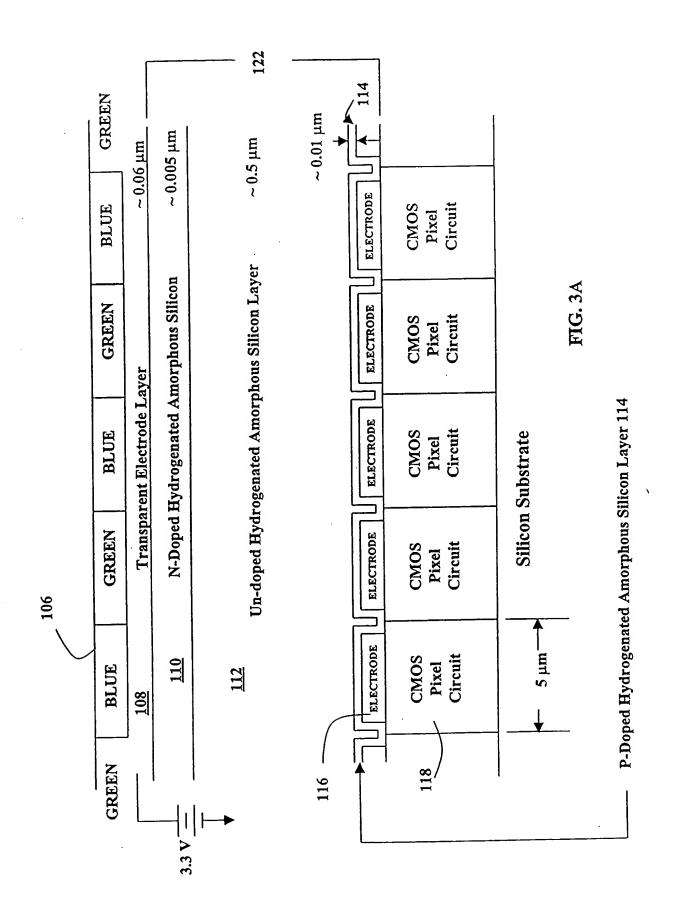
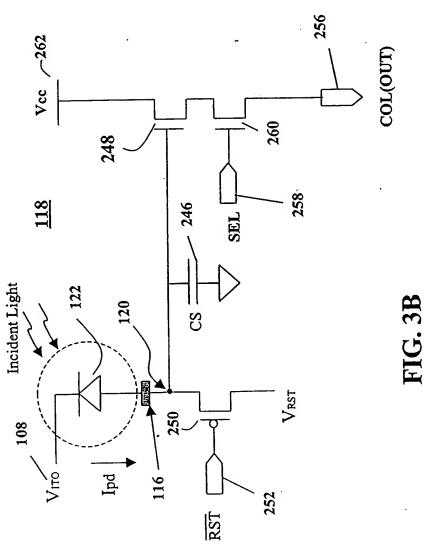


HG.

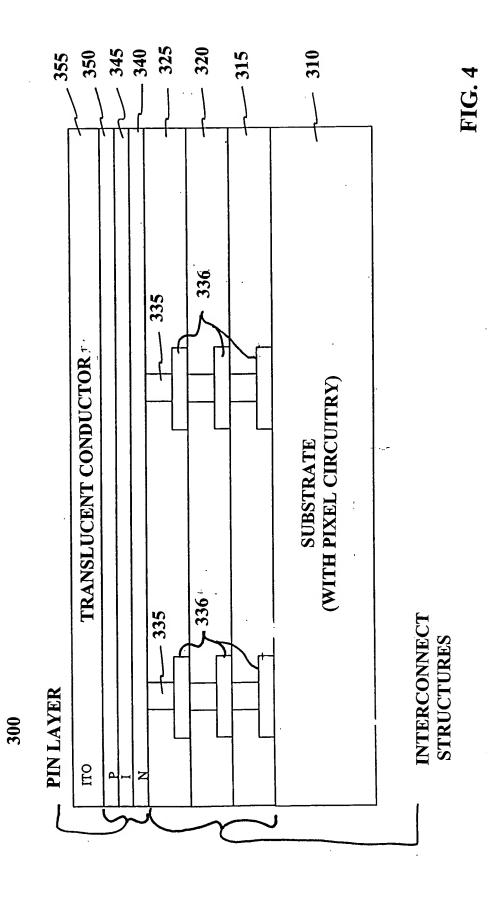


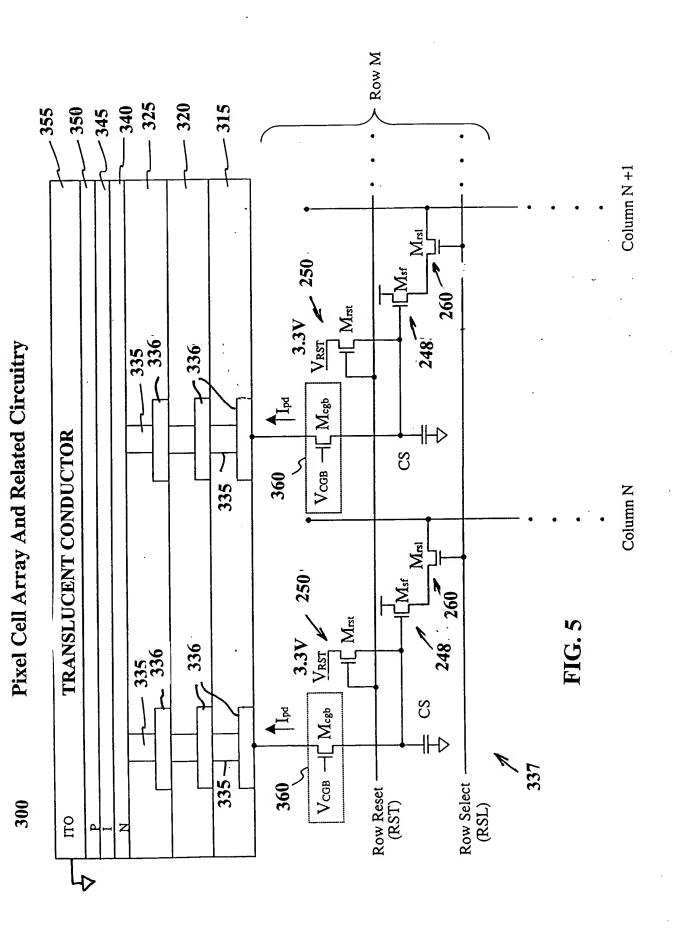
G **U** ~ 4 G Ç Ø В G Ç × α Ç G Ø 8 ტ 2 Ç 2

FIG. 3C

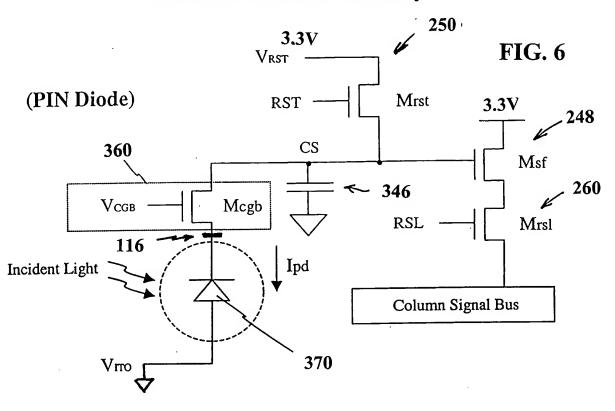


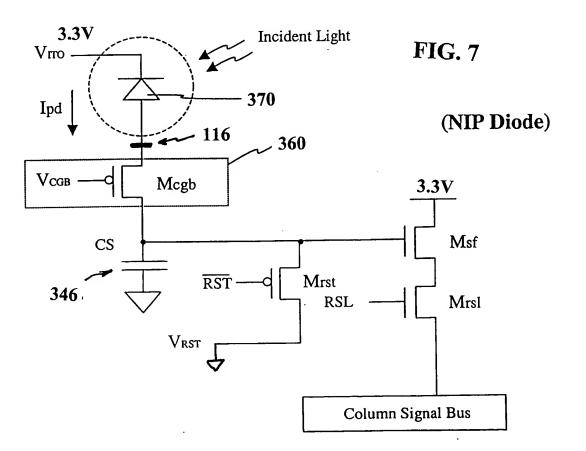
POAP Sensor Pixel Cell Array





Individual Pixel Cell Circuitry





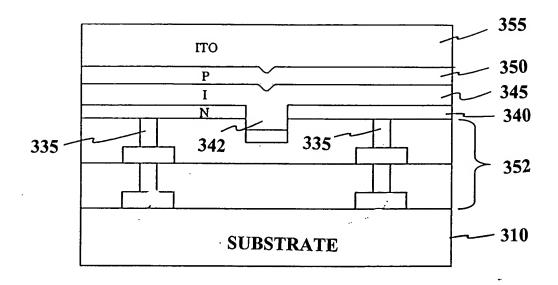


FIG. 8

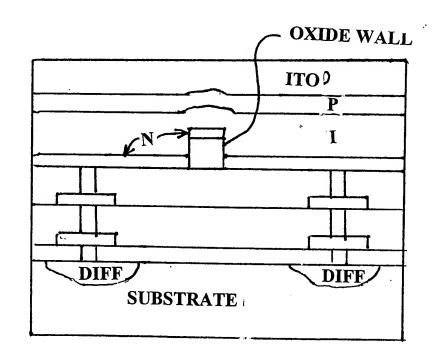


FIG. 8A



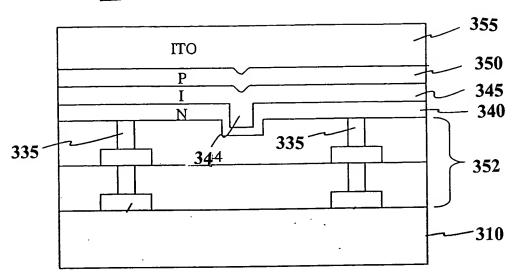


FIG. 9

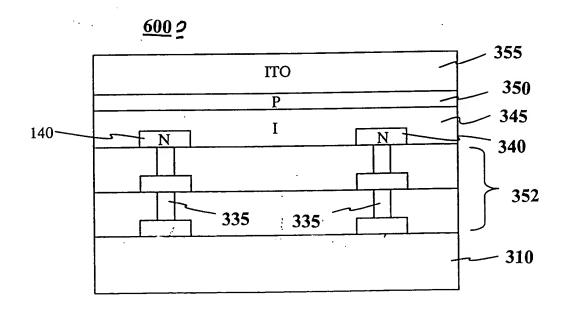


FIG. 10

Individual Pixel Cell Circuitry (Six-Transistor Architecture)

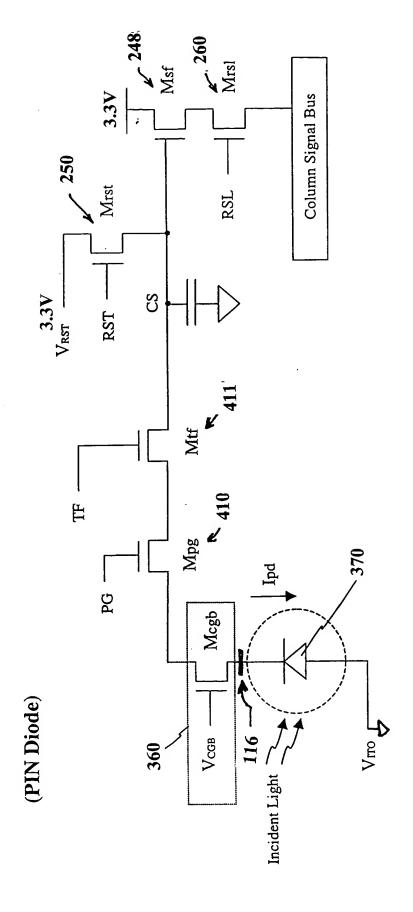
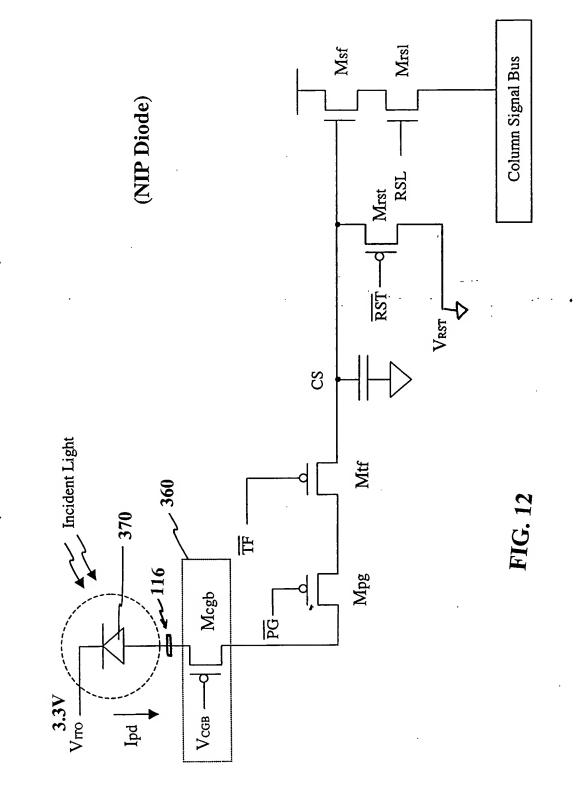
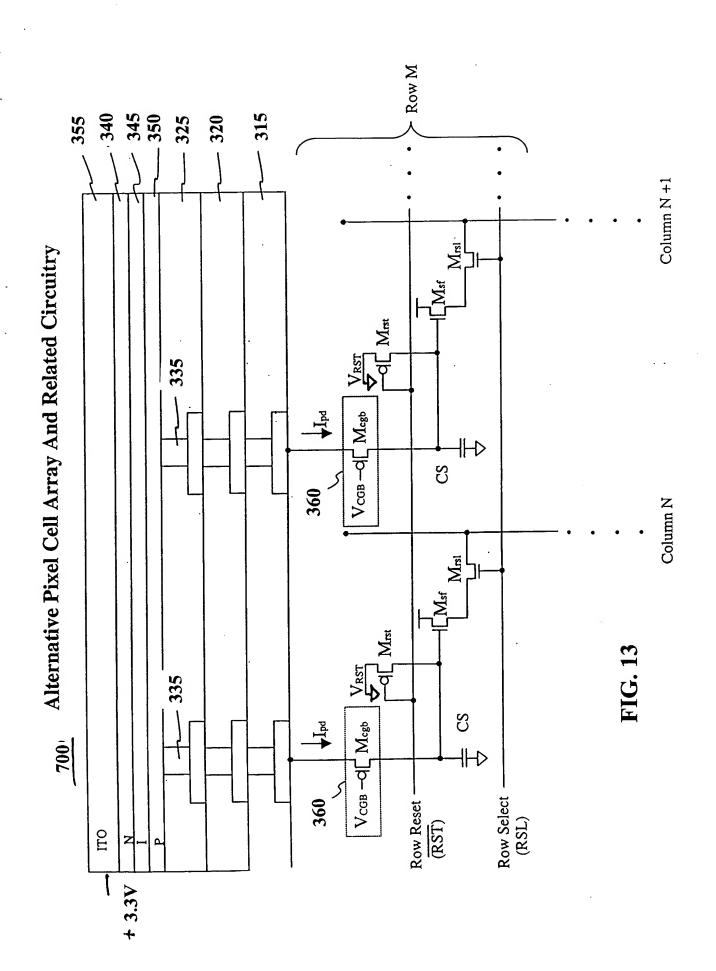


FIG. 11

Individual Pixel Cell Circuitry (Six-Transistor Architecture)





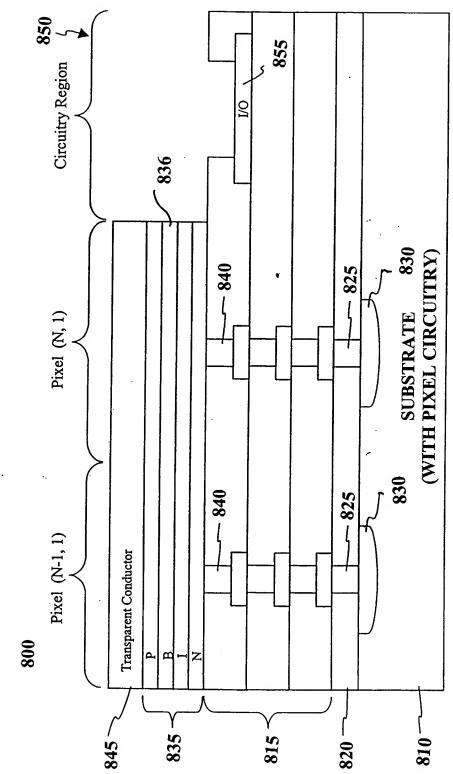


FIG. 14

PBIN Pixel Architecture

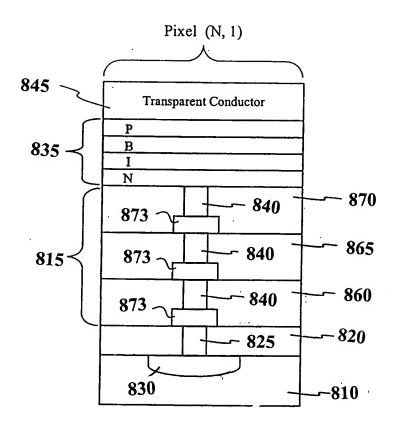
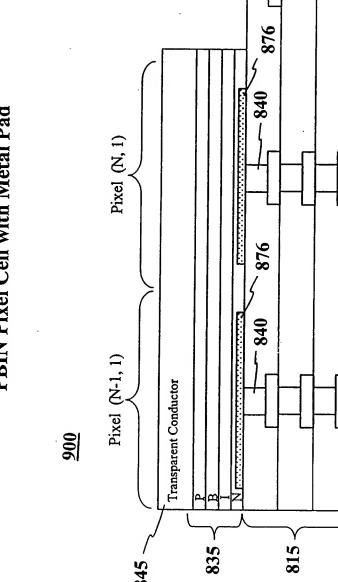


FIG. 15



845

855

-825

-825

820 -

815 <

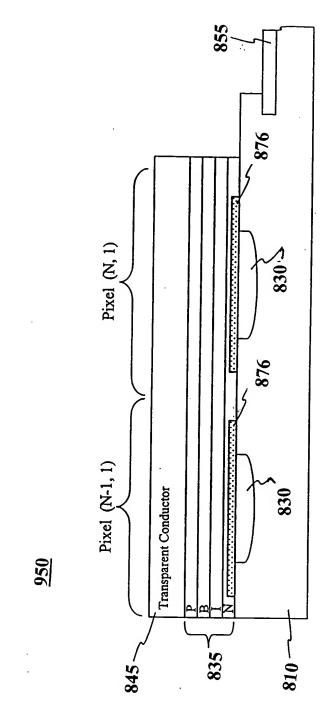
830

-830

810 -

PBIN Pixel Cell with Metal Pad

PBIN Pixel Cell with Metal Pad (Alternative Design)



POAP Sensor Utilizing Isolated Circuitry Region

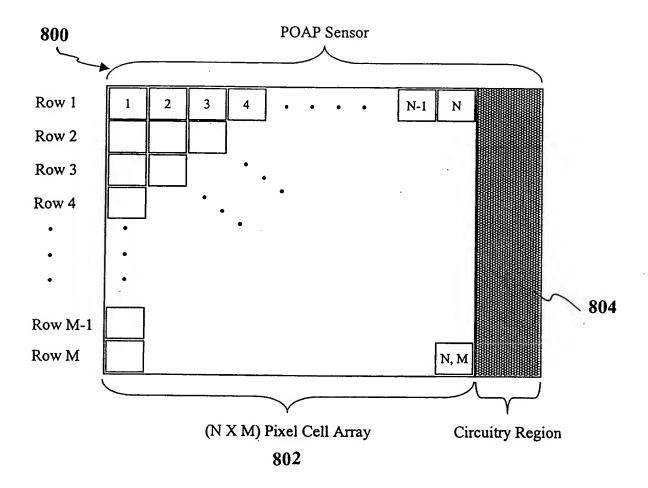


FIG. 17A

POAP Sensor Utilizing Multiple Circuitry Regions

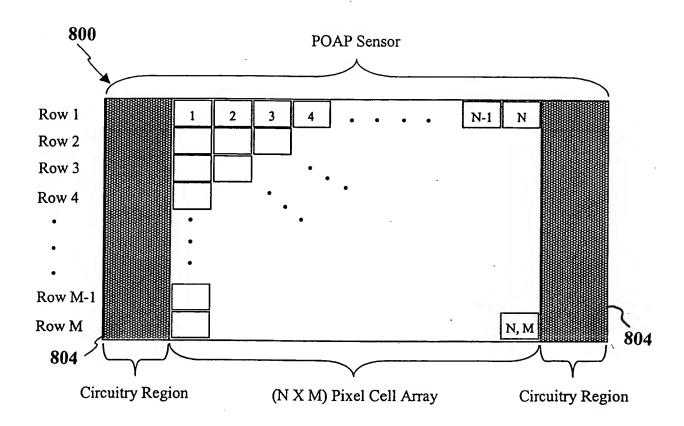


FIG.17B

POAP Sensor Utilizing Four-Sided Circuitry Region

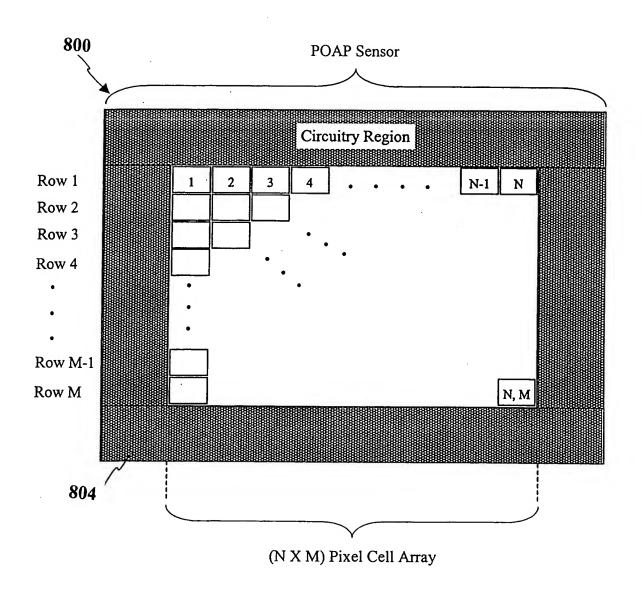


FIG. 17C

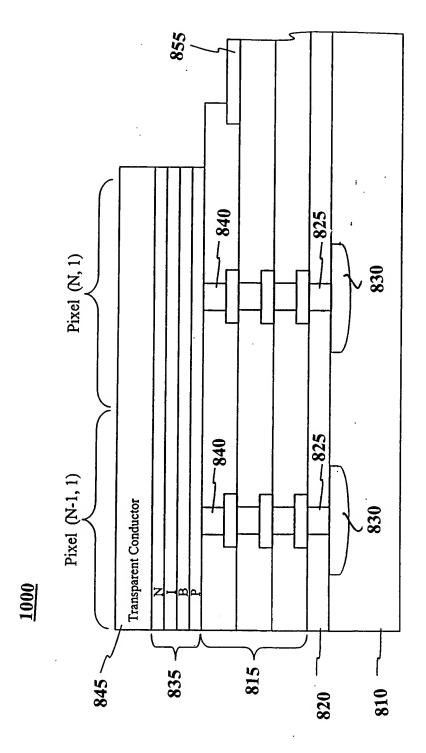
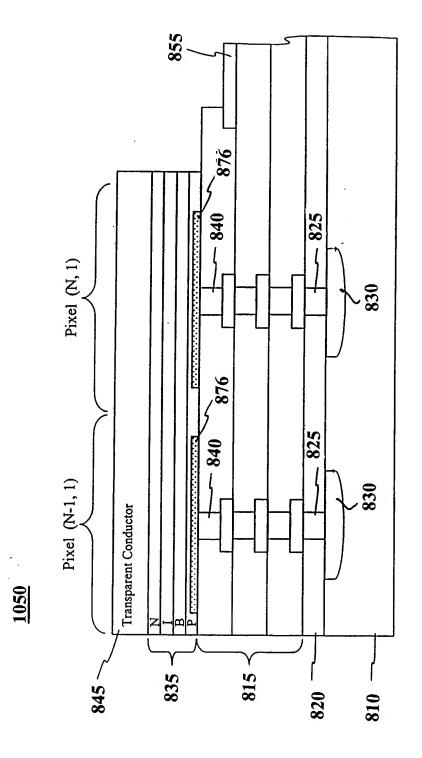


FIG. 18A



NIBP Pixel Cell with Metal Pad

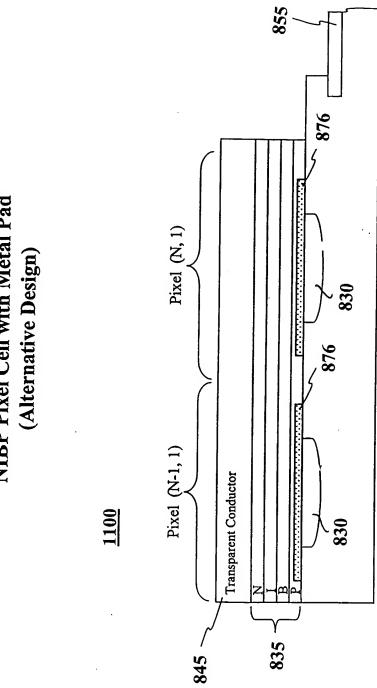


FIG. 18C

POAP Sensor Having A Discontinuous Trench

<u>1150</u>

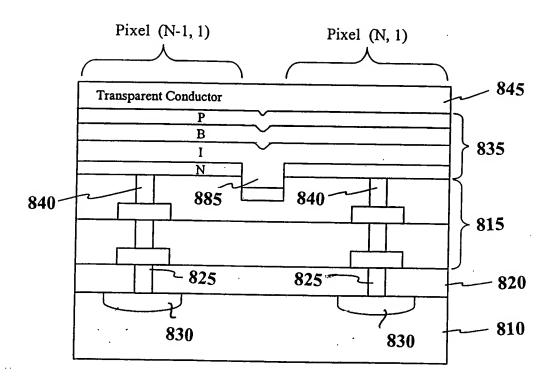
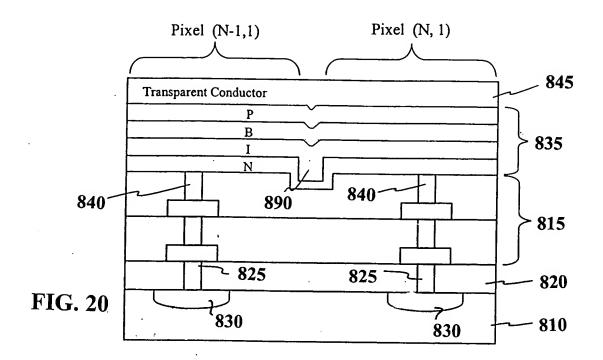
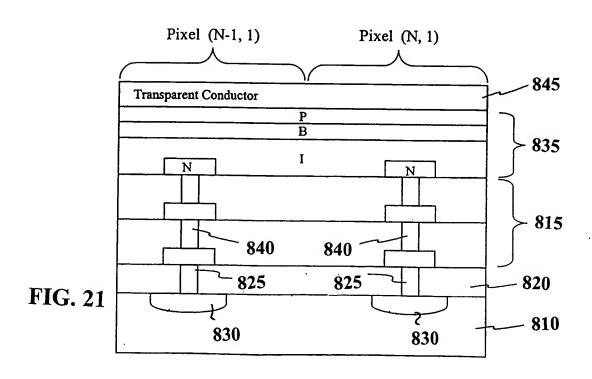
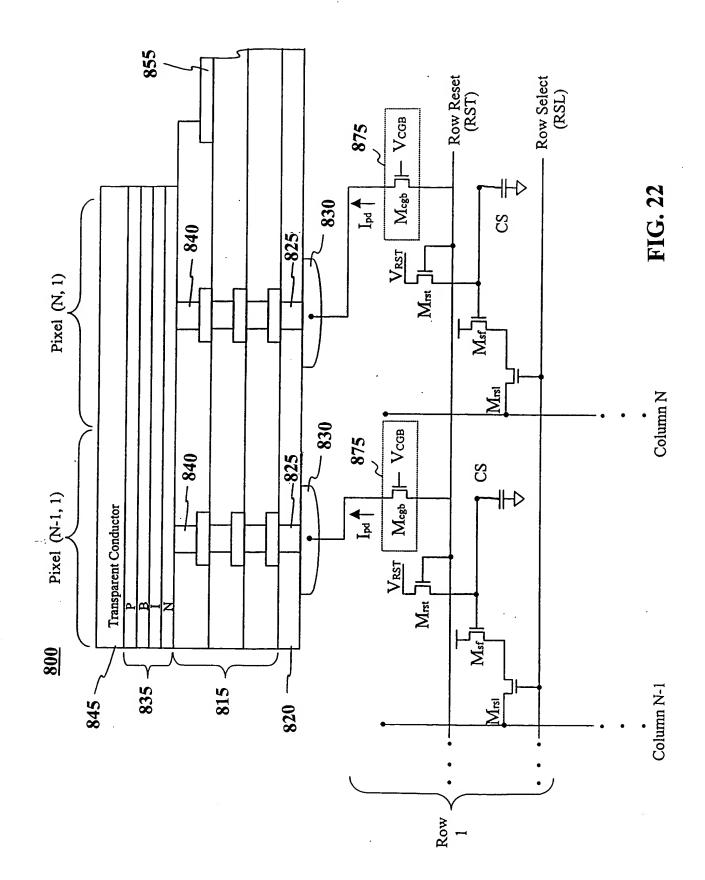


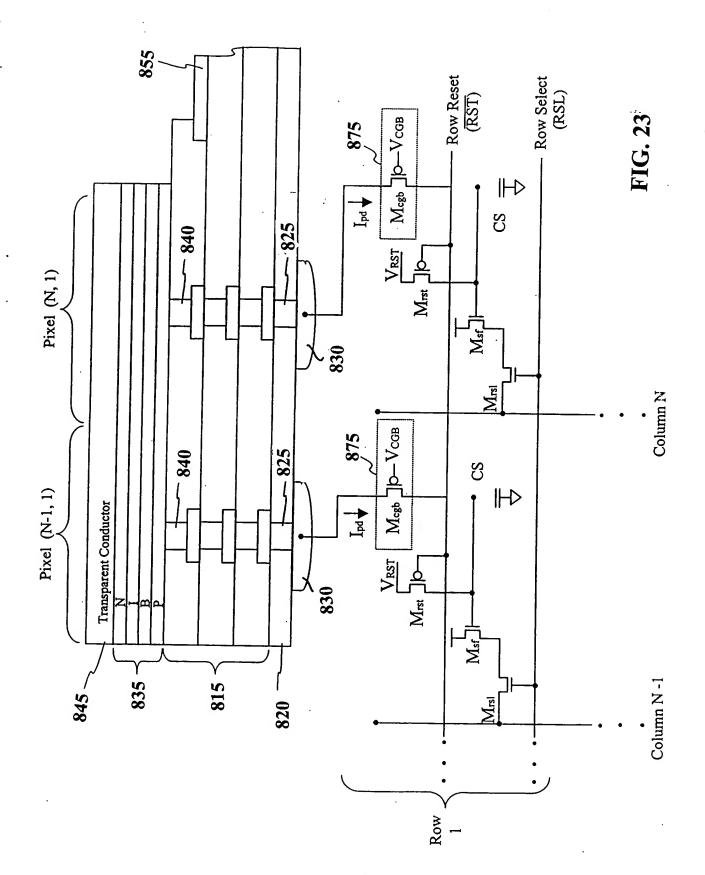
FIG. 19

POAP Sensors Having Trench And Patterned Bottom Layers









Individual Pixel Cell Circuitry (Six-Transistor Architecture)

(PBIN Diode)

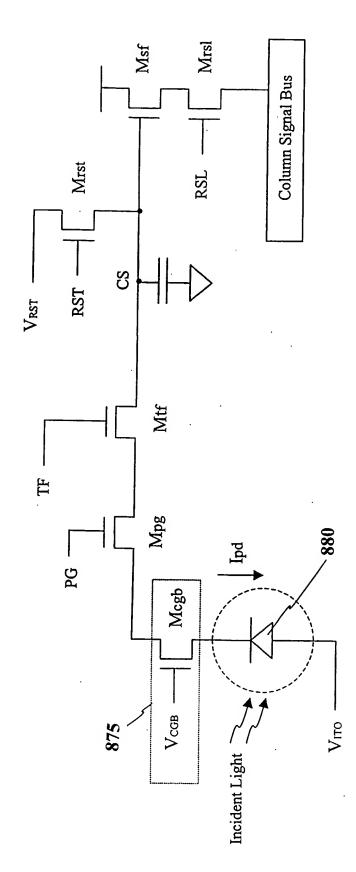
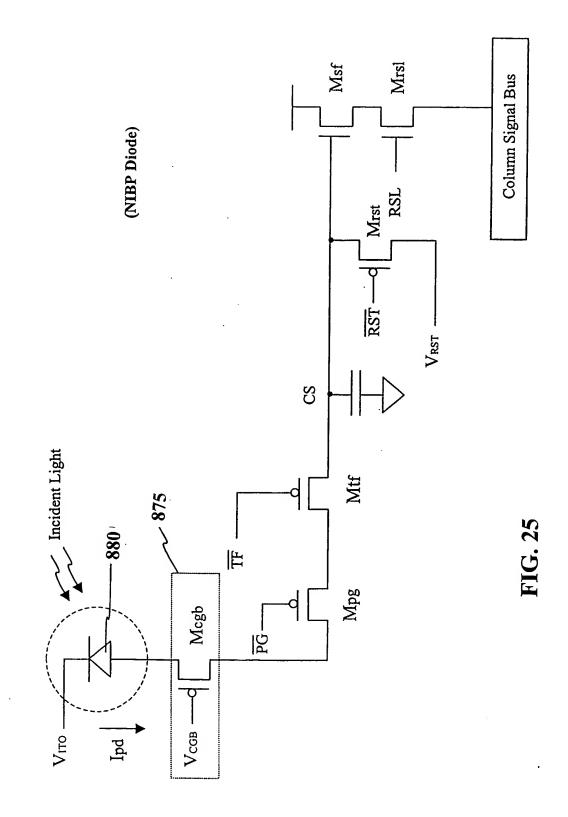
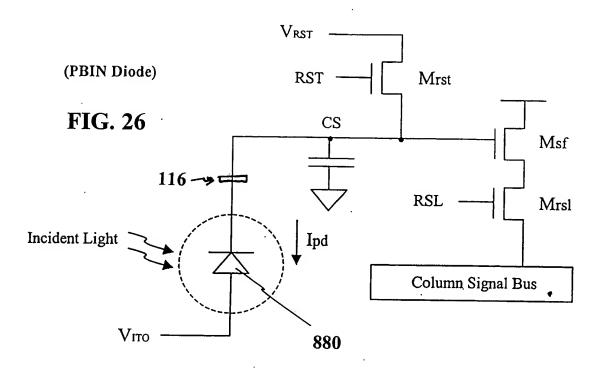


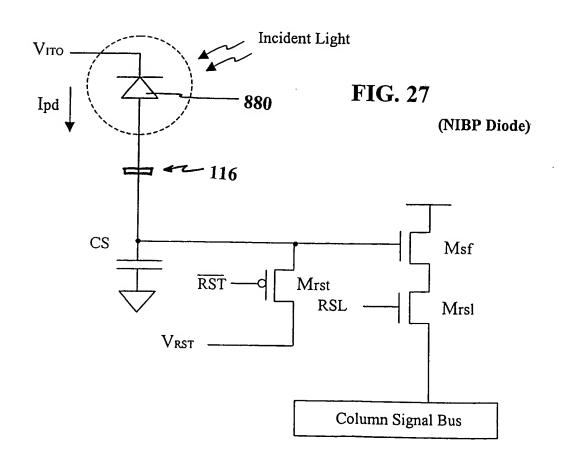
FIG. 24

Individual Pixel Cell Circuitry (Six-Transistor Architecture)



Individual Pixel Cell Circuitry Without GBT





Individual Pixel Cell Circuitry Without GBT (Five-Transistor Architecture)

(PBIN Diode)

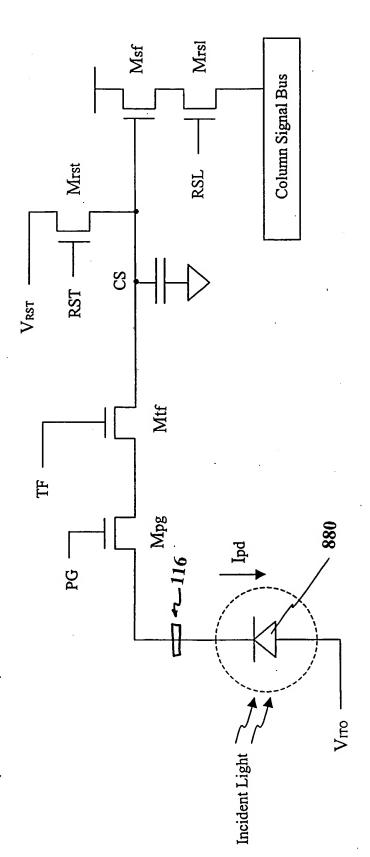
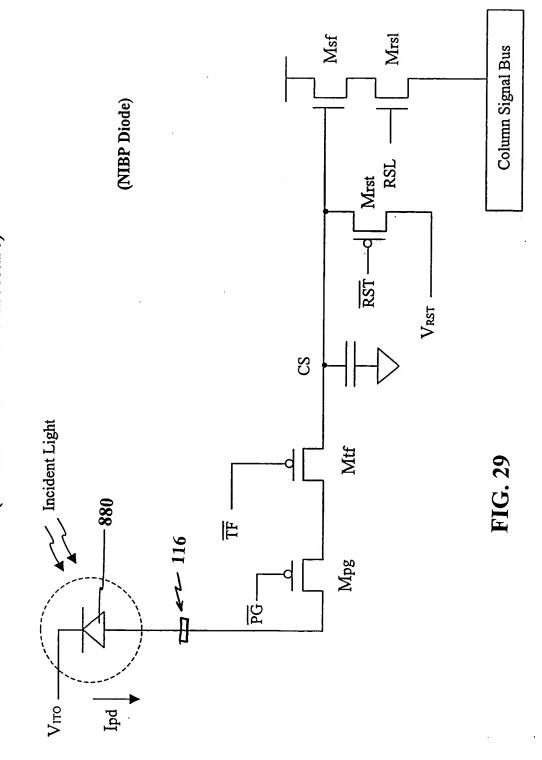
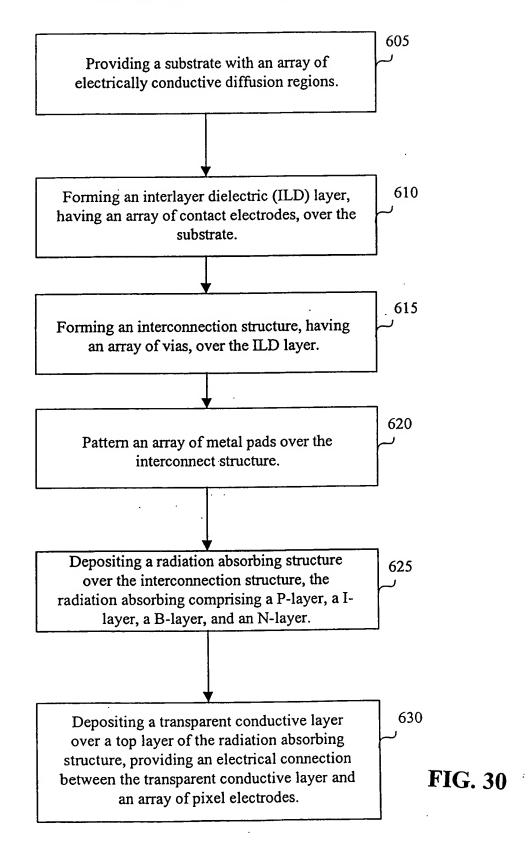


FIG. 28

Individual Pixel Cell Circuitry Without GBT (Five-Transistor Architecture)



Fabricating A POAP Sensor



POAP Sensor Fabrication

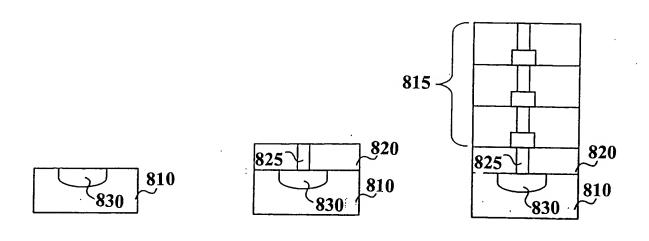


FIG. 31A

FIG. 31B

FIG. 31C

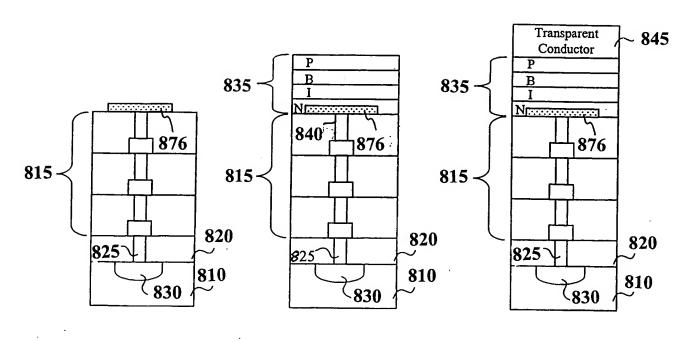


FIG. 31D

FIG. 31E

FIG. 31F